Timeline

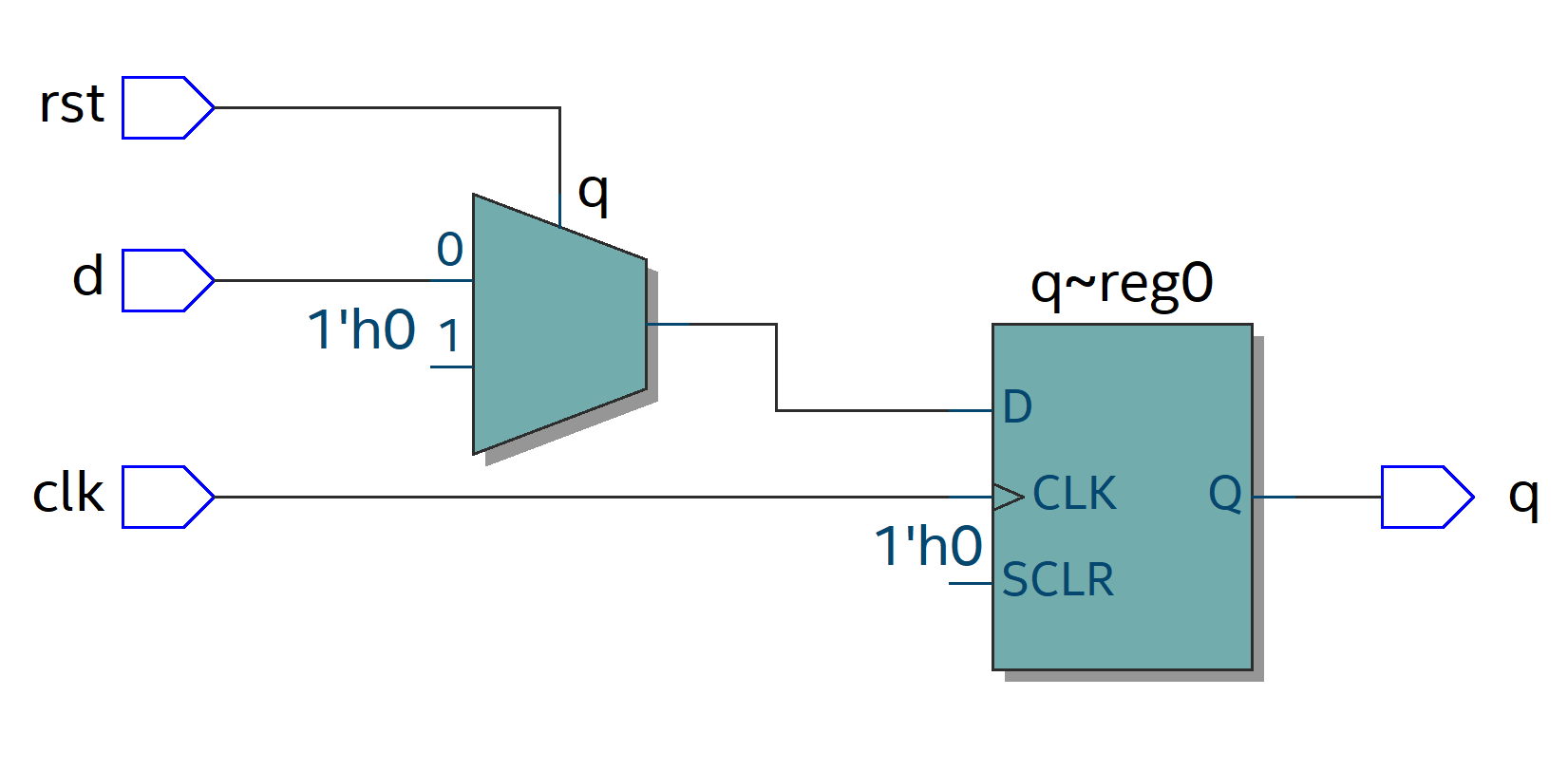
Description automatically generated

Code 1:

Diagram

Description automatically generated

Code 2:



Code 3:

Chart, diagram

Description automatically generated

Step 3:

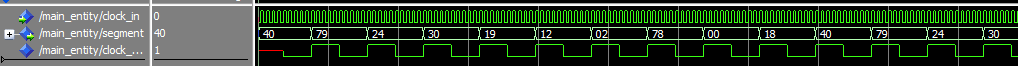
A screenshot of a computer

Description automatically generated with medium confidence

Graphical user interface, text

Description automatically generated

Part 2:



radix hexadecimal  
force clock\_in 0 0 ns, 1 {20 ns} -r 40 ns   
run 8000 ns

Need vhdl code